

Form PTO-1449 U.S. Dept. of Commerce Patent & Trademark Office 17 2005 List of Documents Initialed by Applicant (Use several sheets if necessary)	Atty. Docket: 02-GR2-175	Serial No. 10/649,998
	Applicant: Lionel GRILLO	
	Filing Date: August 26, 2003	Group: 2817






U.S. PATENT DOCUMENTS

[illegible]

FOREIGN PATENT DOCUMENTS

[illegible]

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

	AA1	P.R. Gray et al. "Analysis and Design of Analog Integrated Circuits", March 2001, New York, Chapter 4, pp. 287-293 and Chapter 12, pp. 808-832, XP002241157.
	AA2	Menolfi C. et al. "A Low-Noise CMOS Instrumentation Amplifier for Thermoelectric Infrared Detectors", IEEE Journal of Solid-State Circuits, IEEE, Inc. New York, Vol. 32, No. , July 1997, pp. 968-976, XP000729356.
	AA3	Gopinathan V. et al. "Design Considerations and Implementation of a Programmable High-Frequency Continuous-Time Filter and Variable-Gain Amplifier in Submicrometer CMOS", IEEE Journal of Solid State circuits, IEEE Inc., New York, Vol. 34, No. 12, December 1999, pp. 1698-1707, XP000932500.
	AA4	Daubert S. J. et al. "A Transistor-Only Current-Mode Sigma Delta Modulator", IEEE Journal of Solid State Circuits, IEEE Inc., New York, Vol. 27, No. 5, May 1992, pp. 821-830, XP000291092.
	AA5	French Preliminary Search Report dated May 14, 2003 for French Application No. 0210751.

Examiner:

NGUYEN, KV

Date Considered:

9/1/05

EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609; Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.

